

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	11	330/64-65.ccls. and (power adj amplifier adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:25
L8	4	7 and module	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:24
L9	0	(power adj amplifier adj circuit) and (multi adj stage adj cascade)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:43
L10	221	(power adj amplifier adj circuit) and (final adj stage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:26
L11	14	(power adj amplifier adj circuit) and (final adj stage adj transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:27
L12	13	(power adj amplifier adj circuit) and (final adj stage adj transistor) and control	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:28
L13	1696	257/700-730.ccls. and (circuit adj board) and (semiconductor adj chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:29
L14	147	257/700-730.ccls. and (circuit adj board) and (second adj semiconductor adj chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:29
L15	70	257/700-730.ccls. and (circuit adj board) and (second adj semiconductor adj chip) and electrode	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:31

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L16	1102	(power adj amplifier adj module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:31
L17	371	16 and board	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:31
L18	218	16 and board and chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:32
L19	106	16 and board and chip and wire	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:32
L20	33	16 and board and chip and wire and resin	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:40
L21	10	(first adj semiconductor adj chip) with (final adj stage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:40
L22	6	(first adj semiconductor adj chip) with (final adj stage adj transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:40
L23	19	(semiconductor adj chip) with (final adj stage adj transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/07/19 16:41